

IP-4S-RS-R40

4-Slot Backplane with 3 PCI Bus

User Manual Ver. 4.0

1. P8: AT Power Connector

Pin	Description
1	PWRGD
2	+5V
3	+12V
4	-12V
5	GND
6	GND

1	2	3	4	5	6

2. P9: AT Power Connector

Pin	Description
1	GND
2	GND
3	-5V
4	+5V
5	+5V
6	+5V

1	2	3	4	5	6

3. CN1, CN5: Fan Connector

Pin	Description
1	GND
2	+12V
3	NC



4. CN4: Voltage Connector

Pin	Description
1	+5V
2	-5V
3	-12V
4	+12V
5	GND

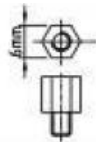
1	2	3	4	5

5. PCI routing table:

PCI ROUTING			
SIGNAL	PCI 1	PCI 2	PCI 3
REQ#	REQ0#	REQ1#	REQ2#
GNT#	GNT0#	GNT1#	GNT2#
CLOCK	CLK0	CLK1	CLK2
IDSEL	AD19	AD20	AD21
INTA#	A	D	C
INTB#	B	A	D
INTC#	C	B	A
INTD#	D	C	B

6. Notice:

- a. Recommend to use 6mm width spacer to install



- b. Torsion must less than 6lb-inch

7. Dimension:

